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PTO/SB/81 (08-04)

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POWER OF ATTORNEY and CORRESPONDENCE ADDRESS INDICATION FORM	Application Number	10/574,304
	Filing Date	9/30/2003
	First Named Inventor	Poechnueller
	Title	Backside of Chip Implementation of Redundancy Fuses and Contact Pads
	Art Unit	2818
	Examiner Name	Gene N. Auduong
Attorney Docket Number		INTECH 3.0-085 (2002 P 50486 US)

I hereby appoint:

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48154

OR

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Name	Registration Number

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<input type="checkbox"/> Firm or Individual Name	Slater & Matsil, L.L.P.				
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I am the:

☐ Applicant/Inventor☒ Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/06).

SIGNATURE of Applicant or Assignee of Record

Signature	I.V. Post	I.V. Ginstenreou	Date	03.03.05
Name	I.V. Post	I.V. Ginstenreou	Telephone	+49/89/234-28413
Title and Company	both authorized Patent administrator Infineon Technologies AG			

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

☒ *Total of 2 forms are submitted.

This collection of information is required by 37 CFR 1.31 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/98 (08-04)

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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Infineon Technologies AGApplication No./Patent No.: 10/674,304 Filed/Issue Date: 9/30/2003Entitled: Backside of Chip Implementation of Redundancy Fuses and Contact PadsInfineon Technologies AG, a corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest.
The extent (by, percentage) of its ownership interest is _____ %
in the patent application/patent identified above by virtue of either:

A. ☐ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel/Frame __, or for which a copy thereof is attached.

OR

B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: Peter Poechmueller To: Infineon Technologies North America Corp.
The document was recorded in the United States Patent and Trademark Office at
Reel 014565, Frame 0834, or for which a copy thereof is attached.
2. From: Infineon Technologies North America Corp. To: Infineon Technologies AG
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☐ Copies of assignments or other documents in the chain of title are attached.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

I.V. Post I.V. Girstenbreu
Signature
I.V. Post I.V. Girstenbreu
Printed or Typed Name
both authorized Patent administrator
Title

03.03.2005

Date

+49/89/234-28413

Telephone Number

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